



Applicant(s): Brian A. Petersen, Mark A. Ross

Assignee: Cisco Technology, Inc.

Title: Network Processor System Including a Central Processor and at least

one Peripheral Processor (As Amended)

Serial No.: 09/469,409

Filing Date:

December 22, 1999

Examiner:

Melvin H. Pollack

Group Art Unit:

2152

Docket No.:

CIS0042US

Austin, Texas November 6, 2002

BOX: NON-FEE AMENDMENT

COMMISSIONER FOR PATENTS

Washington, D. C. 20231

RESPONSE TO NON-FINAL OFFICE ACTION

Dear Sir:

This paper is responsive to the Office action dated August 19, 2002, having a shortened statutory period expiring November 19, 2002. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

RECEIVED
NOV 1 5 2002
Technology Center 2100